



Docket No.: A-64873-1/MSS (463035-350)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

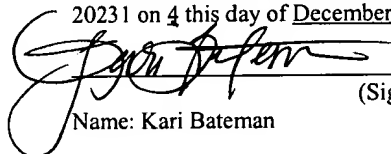
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First Named Inventor:	Jack Chihchieh Yao	Examiner: J. R. Lund
Appln. No.:	09/457,929	
Filing Date:	December 8, 1999	
Title:	Wafer Carrier and Semiconductor Apparatus for Processing a Semiconductor Substrate	
		Group Art Unit: 1763

PETITION FOR EXTENSION OF PERIOD FOR RESPONSE

Commissioner for Patents
Washington, D.C. 20231

I hereby certify that this document is being sent via First Class U.S. mail addressed to: Commissioner for Patents, Washington, D.C. 20231 on 4 this day of December, 2002.


(Signature)
Name: Kari Bateman

Dear Sir:

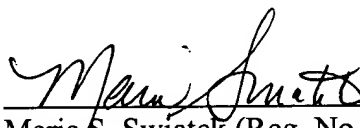
Pursuant to 37 C.F.R. § 1.136(a), Applicant hereby petitions for an extension of 3-month(s) (from September 4, 2002 to December 4, 2002) within which to respond to the Office Action dated June 4, 2002. A check in the amount of \$1,660.00 (\$920.00 for 3 month extension fee; \$740.00 for RCE basic fee) is enclosed herewith to cover the extension fee. Please charge any additional fees or credit any overpayment to our Deposit Account No. 06-1300 (Order No. A-64873-1/MSS (463035-350)).

Respectfully submitted,

DORSEY & WHITNEY LLP

Date: December 4, 2002

By:


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